

PCN Number:	20161003000	PCN Date:	October 6, 2016														
Title:	Datasheet for TPS65320C-Q1																
Customer Contact:	PCN Manager	Dept:	Quality Services														
Change Type:																	
☐ Assembly Site	☐ Design	☐ Wafer Bump Site															
☐ Assembly Process	☒ Data Sheet	☐ Wafer Bump Material															
☐ Assembly Materials	☐ Part number change	☐ Wafer Bump Process															
☐ Mechanical Specification	☐ Test Site	☐ Wafer Fab Site															
☐ Packing/Shipping/Labeling	☐ Test Process	☐ Wafer Fab Materials															
		☐ Wafer Fab Process															
Notification Details																	
Description of Change:																	
<p>Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is updated as seen in the change revision history below:</p>																	
																	
TPS65320C-Q1 SLVSD50C – MARCH 2016 – REVISED SEPTEMBER 2016 www.ti.com																	
<table border="1" style="width: 100%;"> <thead> <tr> <th style="text-align: left;">Changes from Revision B (June 2016) to Revision C</th> <th style="text-align: right;">Page</th> </tr> </thead> <tbody> <tr> <td>• Changed the maximum value for the LDO regulator input range from 36 V to 20 V in the <i>Features</i> section</td> <td style="text-align: right;">1</td> </tr> <tr> <td>• Changed the description for the BOOT pin in the <i>Pin Functions</i> table</td> <td style="text-align: right;">3</td> </tr> <tr> <td>• Changed the maximum supply input voltage for VIN_LDO from 20 to 22 in the <i>Absolute Maximum Ratings</i> table</td> <td style="text-align: right;">4</td> </tr> <tr> <td>• Added a note about the nRST pin to the test conditions for the output high parameter in the <i>Electrical Characteristics</i> table</td> <td style="text-align: right;">7</td> </tr> <tr> <td>• Deleted that the device can operate at high duty cycles under the dropout mode operation from the <i>Overview</i> section</td> <td style="text-align: right;">10</td> </tr> <tr> <td>• Added the <i>Receiving Notification of Documentation Updates</i> section</td> <td style="text-align: right;">34</td> </tr> </tbody> </table>				Changes from Revision B (June 2016) to Revision C	Page	• Changed the maximum value for the LDO regulator input range from 36 V to 20 V in the <i>Features</i> section	1	• Changed the description for the BOOT pin in the <i>Pin Functions</i> table	3	• Changed the maximum supply input voltage for VIN_LDO from 20 to 22 in the <i>Absolute Maximum Ratings</i> table	4	• Added a note about the nRST pin to the test conditions for the output high parameter in the <i>Electrical Characteristics</i> table	7	• Deleted that the device can operate at high duty cycles under the dropout mode operation from the <i>Overview</i> section	10	• Added the <i>Receiving Notification of Documentation Updates</i> section	34
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<p>These changes may be reviewed at the datasheet links provided. http://www.ti.com/lit/ds/symlink/tps65320c-q1.pdf</p>																	
Reason for Change:																	
To more accurately reflect device characteristics.																	
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):																	
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.																	
Changes to product identification resulting from this PCN:																	
None.																	
Product Affected:																	
TPS65320CQPWRQ1																	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com